



COPY OF PAPERS
ORIGINALLY FILED

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Alan R. Reinberg
Title: METHOD FOR REDUCING SINGLE BIT DATA LOSS IN A MEMORY CIRCUIT
Docket No.: 303.522US1
Filed: August 25, 1999
Examiner: Richard A. Booth

Serial No.: 09/382,442
Due Date: June 21, 2002
Group Art Unit: 2812

JUN 10 2002
TECHNOLOGY CENTER 2800

RECEIVED

Box AF
Commissioner for Patents
Washington, D.C. 20231

We are transmitting herewith the following attached items (as indicated with an "X"):

A return postcard.
 An Amendment and Response under 37 CFR § 1.116 (4 Pages).
 Clean Version of Pending Claims (4 pgs.).

Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and
please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938, Minneapolis, MN 55402 (612-373-6900)

By: J. M. Kalis
Atty: Janal M. Kalis
Reg. No. 37,650

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Box AF, Commissioner for Patents, Washington, D.C. 20231, on this 21st day of May, 2002.

Name

Amy Moriarty

Signature

Amy Moriarty

Customer Number 21186

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

P.O. Box 2938, Minneapolis, MN 55402 (612-373-6900)

(GENERAL)

#19/Amndt C
(NE)
Attd
JUN 11 2002

S/N 09/382,442

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Alan R. Reinberg

Examiner: Richard A. Booth

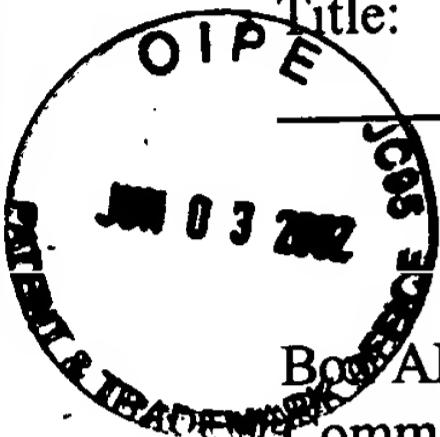
Serial No.: 09/382,442

Group Art Unit: 2812

Filed: August 25, 1999

Docket: 303.522US1

Title: METHOD FOR REDUCING SINGLE BIT DATA LOSS IN A MEMORY
CIRCUIT



AMENDMENT & RESPONSE UNDER 37 C.F.R. § 1.116

Booth AF
Commissioner for Patents
Washington, D.C. 20231

TECHNOLOGY CENTER 2000

JUN 10 2002

RECEIVED

In response to the final Office Action mailed March 21, 2002, please amend the application as follows:

IN THE CLAIMS

Please substitute the claim set in the appendix entitled Clean Version of Pending Claims for the previously pending claim set. The substitute claim set is intended to reflect cancellation of claim 3 and amendment of previously pending claim 1. The specific amendment to the individual claim is detailed in the following marked up set of claims.

1. (Amended) A method for reducing random single bit data loss in a FLASH memory circuit comprising:

providing a semiconductor layer having a surface;
heating the layer in an atmosphere comprising a Hydrogen isotope; and
fabricating a memory circuit comprising single bit data using the semiconductor layer
wherein single bit data loss is reduced.